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## \*BIBDATASHEET\*

Bib Data Sheet

CONFIRMATION NO. 7889

SERIAL NUMBER 10/706,647	FILING DATE 11/12/2003  RULE	CLASS 438	GROUP ART UNIT 2822	ATTORNEY DOCKET NO. 5649-927dv
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## APPLICANTS

Hyoung-Joon Kim, Seoul, KOREA, REPUBLIC OF;

Young-Wook Park, Gyeonggi-do, KOREA, REPUBLIC OF;  
Byeong-Yun Nam, Gyeonggi-do, KOREA, REPUBLIC OF;\*\* CONTINUING DATA \*\*\*\*\* *JMJ*

This application is a DIV of 10/052,721 01/18/2002 PAT 6,680,511

\*\* FOREIGN APPLICATIONS \*\*\*\*\* *JMJ*

REPUBLIC OF KOREA 2001-3066 01/19/2001

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 02/09/2004

Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance <i>Janice M. Thomas</i> Examiner's Signature Initials	STATE OR COUNTRY KOREA, REPUBLIC OF	SHEETS DRAWING 19	TOTAL CLAIMS 12	INDEPENDENT CLAIMS 3

## ADDRESS

20792  
 MYERS BIGEL SIBLEY & SAJOVEC  
 PO BOX 37428  
 RALEIGH , NC  
 27627

## TITLE

Methods of fabricating integrated circuit devices providing improved short prevention

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